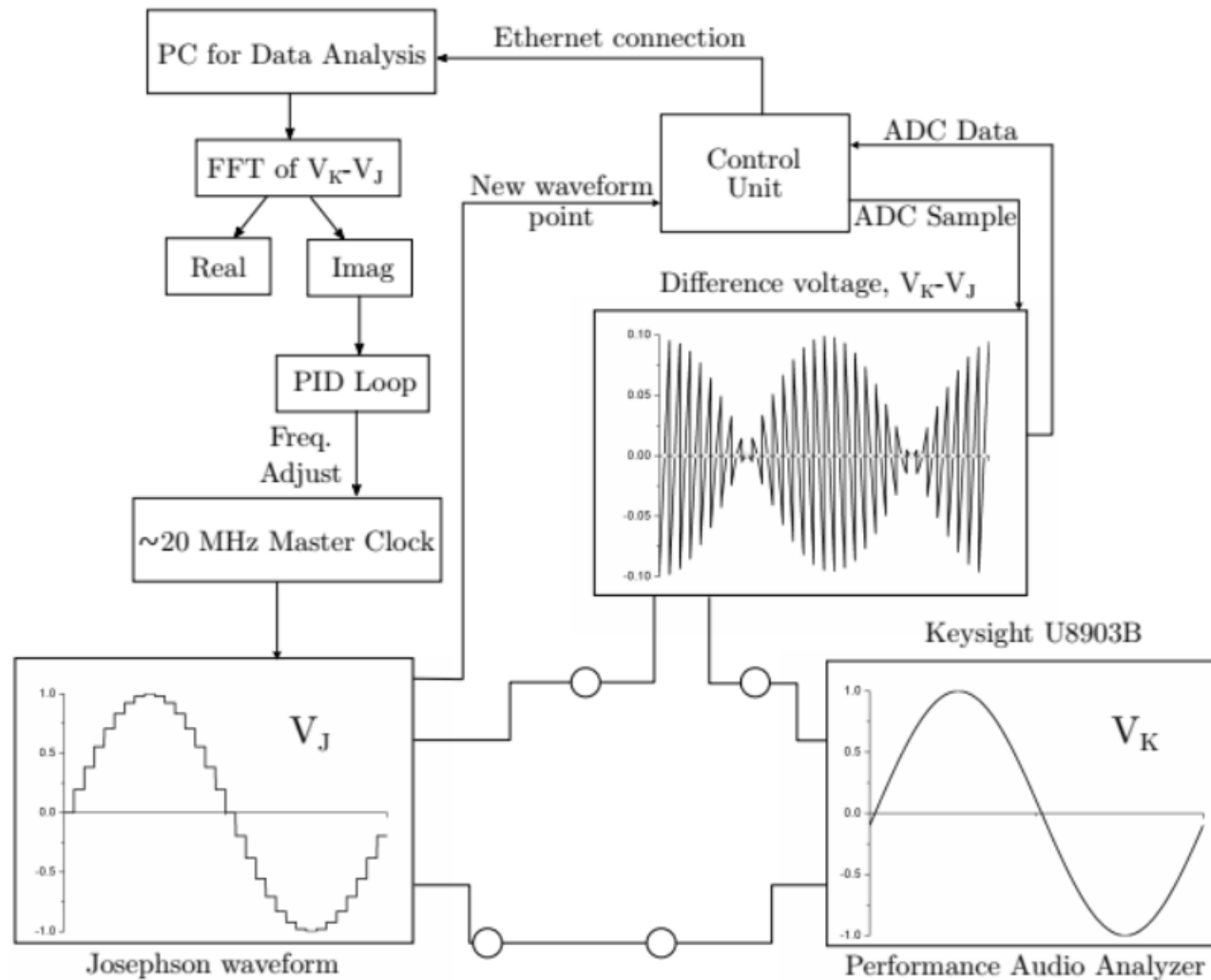


Differential sampling without source lock



- Helge 2 months at NPL summer 2019
- Integration of 10 V bias source with NPL differential sampling system
- Voltage source without phase lock
 - Measure the differential signal
 - Imag part gives phase offset
 - Minimize the value
 - Adjust clock of Bias source
 - Use voltage source as transfer standard